

Currently seeking a position in a growing concern that will benefit from the unification of my strong organizational, business, creative, and technical skills.

## JOSEPH D. MASTROIANNI | LOS GATOS CALIFORNIA 95032

Proven expertise in bringing new products from concept to revenue on schedule and budget. Masterful team builder creating organizations with high output and low turnover.

Expertise in all phases of software development from specification to implementation. Management of geographically separate, multi-national design locations.

Comprehensive background in bringing creative solutions to customer support, business development, partnership administration.

### CAREER TO DATE

3.03 > PRESENT

#### **VP ENGINEERING | RESHAPE INC. | SAN JOSE | CALIFORNIA**

At the request of the ReShape board, engineered the turnaround of the company from a hardware services vendor to an EDA software supplier. Staffed an engineering team. Designed, developed, and delivered ReShape's EDA products on time to spec in 2004. Handled the initial market specs. Managed twenty software engineers in the conception and development of a next-generation product line, delivered on-time to market specifications in 2005. Controlled attrition during company resizing and managed shutdown.

04.02 > 03.03

#### **CONSULTANT/PARTNER | PACKETEER INC. / MAGEE SCIENTIFIC | CUPERTINO / BERKELEY | CALIFORNIA**

As a consultant, developed a tactical 6-month to 2-year plan to enable Packeteer to deliver their next generation OS based product. Proposed staffing, technology and project time line. Devised a scheme to outsource needed software engineering tasks offshore. Evaluated vendors. Declined a permanent position in order to maintain efforts with Magee Scientific providing air quality instrumentation and satellite-based telemetry for remote science applications in Antarctica. Implemented networks, embedded control systems, and sensors in Antarctica.

03.99 > 04.02

#### **VP ENGINEERING | ADAPTIVE SILICON | LOS GATOS | CALIFORNIA**

Was employee number six. Staffed the software engineering team from the ground up. Delivered the software tools to support Adaptive Silicon's MSA 2000 embedded FPGA product line including P&R, Synthesis, and Extraction. Managed twenty software development engineers at the height of Adaptive's product development cycle. Handled reduction in staff. Kept engineers productive and avoided attrition until company shutdown in April of 2002.

03.89 > 03.99

#### **VP ENGINEERING | CADENCE DESIGN SYSTEMS | SAN JOSE | CALIFORNIA**

Managed corporate partnerships with Toshiba, Sony, Harris Semi & SGS Thomson. Business development for Cadence's first Analog Division. Designed and implemented a partnership program for it. Managed partners ST, Nortel, & OKI. Moved into engineering and grew the physical verification product line from \$15M/yr to \$73M/yr through introduction of new products and business development. Founded and managed the Cary, NC. Design center. Built the Design Methodology Engineering Team from scratch. Delivered on Cadence's. ALBA commitments. Managed 120 engineers in 4 locations internationally.

09.87 > 03.89

#### **SR. MANAGER | INTEL | SANTA CLARA | CALIFORNIA**

Managed Intel's physical design CAD group of thirty engineers. Specified and implemented Intel's first in-house floorplanner (ChPPR) as well as the placement and routing system, (DAPR), and the manual schematic and layout editing systems (SEES/FROLIC). Implemented customer support for i486 and i960 design teams. Evaluated and procured third party EDA tools for introduction to the Intel EDA suite. Coordinated development between design sites in Oregon, Folsom, and Israel.

## CAREER TO DATE CONTINUED

05.80 > 09.87

### **SECTION MANAGER / MTS | RCA SOLID STATE TECHNOLOGY CENTER | SOMERVILLE | NEW JERSEY**

Developed CAD software for connectivity verification, symbolic layout of analog chips, and parasitic RC interconnect extraction for digital design. Managed a group of thirteen software developers in the creation of RCA's physical implementation suite of tools for both silicon and silicon-on-sapphire integrated circuit technologies. Managed IT for SSTC. VAX Server Farm + the implementation of desktop computers and in house computer networks. Evaluated and procured 3rd party EDA tools for physical design, verification, and realization of RCA's integrated circuit products, both Analog and Digital.

### **EDUCATION | RUTGERS UNIVERSITY | NEW BRUNSWICK | NEW JERSEY**

B.S.E.E 1981 Concentration in Solid State Physics, 3.7/4.0 cum

M.S.E.E 1984 Concentration in Computer Engineering, 3.5/4.0 cum

## SELECTED PUBLICATIONS

"Chip Level Design Automation" - Network Systems Design

"Scheduled Revolution" - Electronics News

"A Verification System for Cell-Based Design Systems" - Proceedings of the ICCAD

"Extraction of Interconnect Delays from Computer Generated Layouts" - Proceedings of the CICC

"A Quick Turnaround Semicustom Design System" - Proceedings of the CICC

"Fragile Animation" - Tomorrow Magazine

"Jordan's Waterhammer" - Realms of Fantasy

"Antarctica" - Hampton Roads

"The Light and the Weather" - The Antarctic Sun

## ACTIVITIES AND ADDITIONAL ACCOMPLISHMENTS

Member of the United States Antarctic Program 2001-2006. Six field deployments including Taylor Valley, South Pole, and McMurdo Station. Field Survival Training. Implemented the first 802.11 network in the Taylor Valley. Deployed various instruments. Electronics Tech. Software development, C/C++, Java on MacOS, PalmOS, and Windows in the field on the ice.

Published Science Fiction writer - Tomorrow Magazine, Realms of Fantasy

Novelist - Under Contract to Hampton Roads for a novel in 2007

Screenwriter - Finalist in A.P. Sloan / Sundance contest, 2004/5.

Design Automation Conference Executive committee, '89-'91.

Pianist, keyboardist.